Gates Institute of Technology
(UGC-Autonomous Institution)
Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi.
NAAC Accredited with A Grade, NBA Accredited.
NH 44, Gooty, Ananthapuramu Dist. AP-515401

FACULTY PROFILE



Name : **M. KIRAN**
Designation : Assistant Professor
Date of Birth : 10/06/1997
Department : ECE
Highest Qualification : M. Tech (VLSI Design)

Academic Performance

S.No	Degree	University/ Institution	Year of Pass	Specialization	Class
1	M.Tech	JNTUA	2021	VLSI	I
2	B.Tech	JNTUA	2018	E.C.E	I
3	Diploma	State Board of technical education and training	2015	E.C.E	I

1. Total Years of teaching/industry experience till date : 4 Years
2. Date of joining in this Department : 01/07/2026
3. Designation as on date of joining :Assistant Professor

Achievements:

S. No.	Achievements	
1	Number of papers published	-
2	Number of papers presented in conferences	-
3	Number of Projects guided	-
4	Number of FDPs /STTPs/Workshop Attended	3
5	Number of National/ International Conferences Attended	-
6	Others(Book Chapters/Patents etc)	-

Area of Interests:

VLSI, SOC, Digital Circuits, 5G Communications

M. Kiran

Signature of the faculty