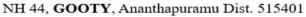


## Gates Institute of Technology Affiliated to JNTUA, Ananthapuramu and Approved by AICTE, New Delhi

## Accredited by NAAC





## **FACULTY PROFILE**

1. Name : P.SRAVANI

2. Designation : Assistant Professor

3. Date of Birth : 06/05/1994

4. Department : ECE

5. Highest Qualification : M.Tech

6. Academic Performance

| S.No | Degree       | University/<br>Institution      | Year of Pass | Specialization    | Class |
|------|--------------|---------------------------------|--------------|-------------------|-------|
| 1    | M.Tech       | JNTUA                           | 2018         | VLSI&ES<br>DESIGN | I     |
| 2    | B.Tech       | JNTUA                           | 2015         | ECE               | I     |
| 3    | Intermediate | Board of Intermediate Education | 2011         | M.P.C             | I     |

7. Total Years of teaching / industry experience till date : 7

: 02/04/2018 8. Date of joining in this Department

9. Designation as on date of joining : Assistant Professor

10. Achivements

| S. No. | Achievemens   |   |
|--------|---|---|
| 1      | Number of papers published                              | 2 |
| 2      | Number of papers presented in conferences               | - |
| 3      | Number of Projects guided                               | - |
| 4      | Number of FDPs / STTPs / Workshop Attended              | - |
| 5      | Number of National / International Conferences Attended | - |
| 6      | Others (Book Chapters / Patents etc)                    | - |

## 11. Area of Interests:

| VLSI |  |  |
|------|--|--|
|      |  |  |

P.SRAVANI