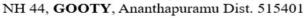


## Gates Institute of Technology Affiliated to JNTUA, Ananthapuramu and Approved by AICTE, New Delhi

## Accredited by NAAC





## **FACULTY PROFILE**

1. Name : M.DIVYA VANI

2. Designation : Assistant Professor

3. Date of Birth : 29/12/1990

4. Department : ECE

5. Highest Qualification : M.Tech

6. Academic Performance

S.No	Degree	University/ Institution	Year of Pass	Specialization	Class
1	M.Tech	JNTUA	2017	VLSI DESIGN	Ι
2	B.Tech	JNTUA	2013	ECE	I
3	Intermediate	Board of Intermediate Education	2008	M.P.C	I

7. Total Years of teaching / industry experience till date : 6

8. Date of joining in this Department : 21/082019

9. Designation as on date of joining : Assistant Professor

10. Achivements

S. No.	Achievemens				
1	Number of papers published	2			
2	Number of papers presented in conferences	2-			
3	Number of Projects guided	4			
4	Number of FDPs / STTPs / Workshop Attended	-			
5	Number of National / International Conferences Attended	-4			
6	Others (Book Chapters / Patents etc)	-			

## 11. Area of Interests:

VLSI		

M.DIVYA VANI

Signature of the faculty